

Milestones from INT-407-PMG-0001

<i>ID Number</i>	<i>Milestone</i>	<i>Person(s)</i>	<i>Due Date</i>	<i>Status</i>
1	Beam size memos	DFB		Delivered
2	Generic Switchyard Requirements	JC		Delivered
3	Devise slide tests	CJ		Complete
4	Survey of commercial slides	CP		Complete
5	Generic Detector Requirements	JSY		Delivered
6	P4S combiner optimization	JC		Complete
7	P4S switchyard optimization	JC		Complete
8	P8 combiner optimization	JC		Complete
9	P8 (internal) switchyard optimization	JC		Complete
10	P4/P8 manufacturer dialogue	JC	<i>Due 2006/07/31</i>	70% done
11	P4S/P8 spectrograph optimization	CJ	<i>Due 2006/09/01</i>	70% done
12	I4S combiner optimization	FB	<i>Due 2006/07/17</i>	90% done
13	I4S switchyard optimization	JC		80% done
14	I6 combiner optimization	FB	<i>Due 2006/07/17</i>	90% done
15	Optical interface with fringe tracker	JC/CJ	<i>Due 2006/08/14</i>	80% done
16	Slide tests Phase 1 (1 slide, tip-tilt repeatability)	CP	<i>Due 2006/08/14</i>	Complete
17	Spatial filter evaluation	MCE	<i>Due 2006/09/11</i>	50% done
18	Alignability evaluation	JC/CJ	<i>Due 2006/10/23</i>	10% done
19	Modulator requirements	DFB	<i>Due 2006/07/31</i>	0% done
20	Modulator tip-tilt tests	JC	<i>Due 2006/09/25</i>	20% done
21	Signal-to-noise calculation	JSY	<i>Due 2006/10/09</i>	30% done
22	Slide tests Phase 2 (2 slides, OPD repeatability)	CP	<i>Due 2006/10/16</i>	Awaiting slide
23	Imaging speed calculation	JSY	<i>Due 2006/10/23</i>	Waiting on 21
24	Costing	JSY	<i>Due 2006/10/23</i>	Waiting on 10
25	Final Report	JSY	<i>Due 2006/11/06</i>	
26	Preliminary design for downselected concept	FB/JC	<i>Due 2006/11/27</i>	